



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

28158

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Re the Application of:

Bailey et al.

Docket No.: TI-27935

Serial No.: 09/452,691

Examiner: Fenty, J.

Filed: 12/02/99

Art Unit: 2815

For: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED
WITHIN A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER
METAL INTERCONNECT LAYER

EXTENSION OF TIME

Assistant Commissioner
for Patents
Washington, DC 20231

Dear Sir:

MAILING CERTIFICATE

I hereby certify that this correspondence is being
deposited with the United States Postal Service as first
class mail in an envelope addressed to: Assistant
Commissioner for Patents, Washington, DC 20231.

11-1-01

Date

Marianna Smith

Marianna Smith

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner
for an extension of the shortened statutory period for response in the above-identified
Application.

The fee for this extension is indicated below:

_____ One Month (\$110)
 X Two Months (\$400)
_____ Three Months (\$920)
_____ Four Months (\$1440)

Please charge the fee to deposit account no. 20-0668. Any further necessary
extension of time is hereby requested. Charge any and all fees to deposit account
no. 20-0668. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated
P.O. Box 655474, M/S 3999
Dallas, TX 75265
(972) 271-1176
Fax: (972) 917-4418

Respectfully submitted,

Jacqueline J. Garner
Registration No. 36,144
Attorney for Applicants

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